Script generated by TTT

Title: Simon: Programmiersprachen (16.11.2012)

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Pages: 105

Definition: Sequential Consistency

Definition (Sequential Consistency Condition for Multi-Processors)

The result of any execution is the same as if the operations of all the processors were executed in some sequential order, and the operations of each individual processor appear in this sequence in the order specified by its program.

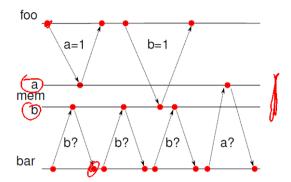
Uh?

Sequential Consistency



Note: there is no observable change if calculations on different memory locations can happen in parallel.

• model each memory location as different process



Some observations:

- the accesses of foo to a occurs before b
- the first two read accesses to b are in parallel to a=1

Memory Consistency

equential Consistency

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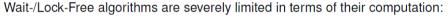
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- show that total orderings C' exist for \rightarrow for which the result differ

Memory Consisten

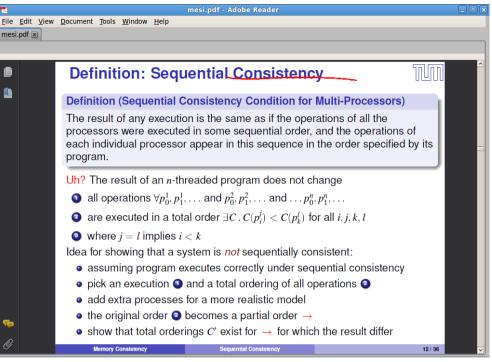
Sequential Consisten

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Limitations of Wait- and Lock-Free Algorithms



- restricted to the semantics of a single atomic operations
- set of atomic operations is architecture specific, but often includes
 - exchange of a memory cell with a register
 - ▶ compare-and-swap of a register with a memory cell
 - fetch-and-add on integers in memory
 - modify-and-test on bits in memory



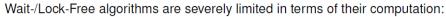
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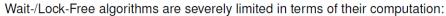
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We will collectively refer to these data structures as locks.

Atomic Executions, Locks and Monitors

Wait-Free Synchronization

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Semaphores and Mutexes

A (counting) *semaphore* is an integer s with the following operations:

A counting semaphore can track how many resources are still available.

Atomic Executions, Locks and Monitors

ocked Atomic Executions

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Semaphores and Mutexes

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    bool avail;
    do {
    void signal() {
        atomic {
            avail = s>0;
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            }
            while (!avail);
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Atomic Executions, Locks and Monitors

Locked Atomic Execution

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Atomic Executions, Locks and Monitors Locked Atomic Executions

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Locked Atomic Executions

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Implementation of Semaphores



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Atomic Executions, Locks and Monitors

Locked Atomic Executions

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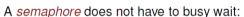
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Atomic Executions, Locks and Monitors Locked Atomic Executions

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Atomic Executions, Locks and Monitors

Locked Atomic Executions

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Atomic Executions, Locks and Monitors

Locked Atomic Execution

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- ullet the operating system lets t return from its call to ${\tt de_schedule}()$

Practical Implementation of Semaphores



Certain optimisations are possible:

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In general, the implementation is more complicated

wait() may busy wait for a few iterations

Atomic Executions, Locks and Monitors

Locked Atomic Execution

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Atomic Executions, Locks and Monitors

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 - saves de-scheduling if the lock is released frequently

Atomic Executions, Locks and Monitors

Locked Atomic Executions

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 - better throughput for semaphores that are held for a short time

Atomic Executions, Locks and M

Locked Atomic Execution

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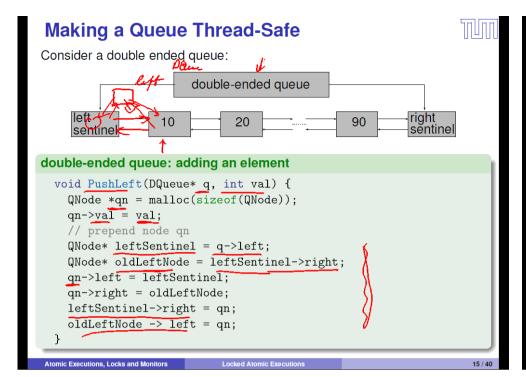
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- wait() may busy wait for a few iterations
 - saves de-scheduling if the lock is released frequently
 - better throughput for semaphores that are held for a short time
- signal() might have to inform the OS that s has been written
 using a semaphore with a single thread reduces to if (s) s--; s++;
- using semaphores in sequential code has no or little penalty
- program with concurrency in mind?

Atomic Executions, Locks and Monitors

ocked Atomic Executions



Mutexes

One common use of semaphores is to guarantee mutual exclusion.

• in this case, a binary semaphore is also called a mutex

Atomic Executions, Locks and Monitor

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- add a lock to the double-ended queue data structure

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- in this case, a binary semaphore is also called a *mutex*
- add a lock to the double-ended queue data structure
- decide what needs protection and what not

double-ended queue: thread-safe version

```
void PushLeft(DQueue*(q) int val) {
   QNode *qn = malloc(sizeof(QNode));
   qn->val = val;
   wait(q->s); // wait to enter the critical section
   QNode* leftSentinel = q->left;
   QNode* oldLeftNode = leftSentinel->right;
   qn->left = leftSentinel;
   qn->right = oldLeftNode;
   leftSentinel->right = qn;
   oldLeftNode -> left = qn;
   signal(q->s); // signal that we're done
}
```

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Atomic Executions, Locks and Monitors

Locked Atomic Executions

Implementing the Removal

By using the same lock q->s, we can write a thread-safe Por Right

```
double-ended queue: removal
 int PopRight(DQueue* q, int val) { Wa
   QNode* oldRightNode;
   wait(q->s); // wait to enter the critical section
   QNode* rightSentinel = q->right;
   oldRightNode = rightSentinel->left;
   if (oldRightNode==leftSentinel) { signal(q->s); return -1; }
   QNode* newRightNode = oldRightNode->left;
   newRightNode->right = rightSentinel;
   rightSentingel->left = newRightNode;
   signal(q->s); // signal that we're done
    int val = oldRightNode->val;
   free(oldRightNode);
   return val;
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  int PopRight(DQueue* q, int val) {
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    wait(g->s); // wait to enter the critical section
    QNode* rightSentinel = q->right;
    oldRightNode = rightSentinel->left;
    if (oldRightNode==leftSentinel) { signal(q->s); return -1; }
    QNode* newRightNode = oldRightNode->left;
    newRightNode->right = rightSentinel;
    rightSentingel->left = newRightNode;
    signal(q->s); // signal that we're done
```

- abstract common concept: take lock on entry, release on exit

Atomic Executions, Locks and Monitors Locked Atomic Executions

return val;

Monitors: An Automatic, Re-entrant Mutex



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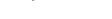
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Atomic Executions, Locks and Monitors

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Implementation of a Basic Monitor

- a procedure associated with a monitor acquires a lock on entry and releases it on exit
- if that lock is already taken, proceed if it is taken by the current thread

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Monitor: a mechanism to address these problems:

- a procedure associated with a monitor acquires a lock on entry and releases it on exit
- if that lock is already taken, proceed if it is taken by the current thread → need a way to release the lock after the return of the last recursive call

A monitor contains a mutex s and the thread currently occupying it:

```
typedef struct monitor mon_t;
                                                           5=1
   struct monitor { int tid; int count; };
   void monitor_init(men_t* m) {, memset(m, 0, sizeof(mon_t)); '}
Define monitor_enter and monitor_leave:
```

ensure mutual exclusion of accesses to mon_t

- track how many times we called a monitored procedure recursively
- void monitor_enter(mon_t *m) { void monitor_leave(mon_t *m) { atomic { assert (m stid bool mine = false; while (!mine) { m->count--: == {| handie | | } atomic { if (m->count==0) { mine = thread_id()==m->tid; // wake up threads if (mine) m->count++; else m->tid=0: if (m->tid==0) { not below mine = true; count= m->tid = thread id() (!mine) de_schedule(&m->tid);(}]

Rewriting the Queue using Monitors



Instead of the mutex, we can now use monitors to protect the queue:

```
double-ended queue: monitor version

void PushLeft(DQueue* q, int val) {
    monitor_enter(q->m);
    ...
    monitor_leave(q->m);
}

void ForAll(DQueue* q, void* data, void (*callback)(void*,int)){
    monitor_enter(q->m);
    for (QNode* qn = q->left->right; qn!=q->right; qn=qn->right)
        (*callback)(data, qn->val);
    monitor_leave(q->m);
}
```

Recursive calls possible:

Atomic Executions, Locks and Monitors

Locked Atomic Executions

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}
```

Recursive calls possible:

- the function passed to ForAll can invoke PushLeft
- example: ForAll(q,q,&PushLeft) duplicates entries

Atomic Executions, Locks and Monitor

Locked Atomic Execution

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Rewriting the Queue using Monitors



Instead of the mutex, we can now use monitors to protect the queue:

Recursive calls possible:

- the function passed to ForAll can invoke PushLeft
- ullet example: ForAll(q,q,&PushLeft) duplicates entries
- using monitor instead of mutex ensures that recursive call does not block

Condition Variables



✓ Monitors simplify the construction of thread-safe resources. Still: Efficiency problem when using resource to synchronize:

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Atomic Executions, Locks and Monitors

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Atomic Executions, Locks and Monitors

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struct monitor { int s; int tid; int count; int cond; };

Define these two functions:

- wait for the condition to become true
 - called while being inside the monitor
 - temporarily releases the monitor and blocks when signalled, re-acquires the monitor and returns
- signal waiting threads that they may be able to proceed

one/all waiting threads that called wait will be woken up, two possibilities: signal-and-urgent-wait: the signalling thread suspends and continues once the signalled thread has released the monitor

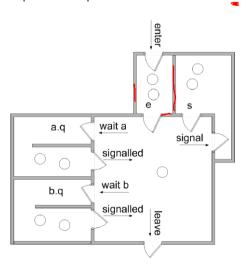
signal-and-continue the signalling thread continues, any signalled thread enters when the monitor becomes available

tions, Locks and Monitors

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Signal-And-Urgent-Wait Semantics

Requires one queues for each condition c and a suspended queue s:

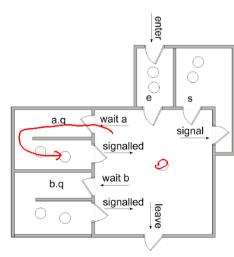


SOUTCO: http://en.wikipedia.org/wiki/Monitor_(synchronization)

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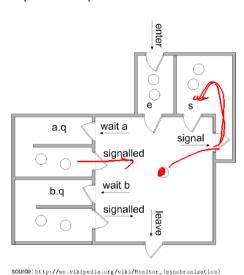


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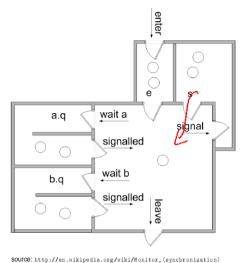
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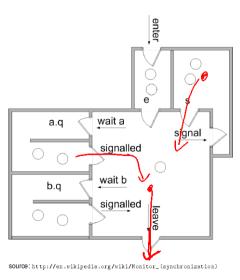
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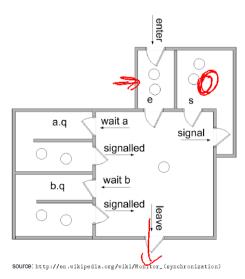
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Atomic Executions, Locks and Monitors

ocked Atomic Executions

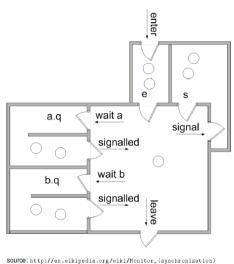
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Atomic Executions, Locks and Monitors

Locked Atomic Executions

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- signal on a is a no-op if a.q is empty
- ullet if a thread leaves, it wakes up one thread waiting on s
- if s is empty, it wakes up one thread from e

 \rightsquigarrow queue s has priority over e

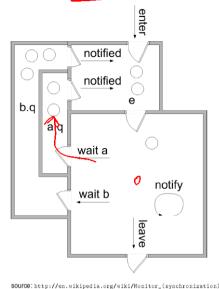
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Signal-And-Continue Semantics

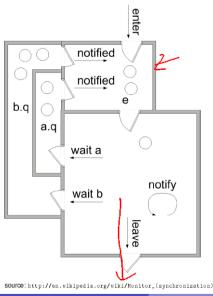
Here, the signal function is usually called notify.



 a call to wait on condition a adds thread to the queue a.q

Signal-And-Continue Semantics

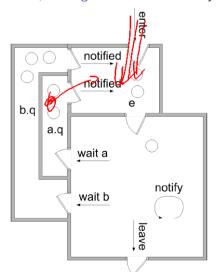
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 \leadsto signalled threads compete for the monitor

SOUICE: http://en.wikipedia.org/wiki/Monitor_(synchronization)

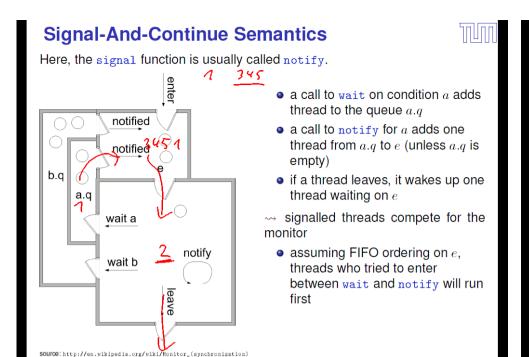
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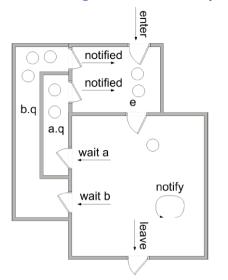
Atomic Executions, Locks and Monitors

Locked Atomic Execution









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- → signalled threads compete for the monitor
 - assuming FIFO ordering on e, threads who tried to enter between wait and notify will run
 - need additional queue s if waiting threads should have priority

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Implementing Condition Variables

Atomic Executions, Locks and Monitors

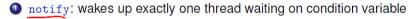
We implement the simpler signal-and-continue semantics:

• a notified thread is simply woken up and competes for the monitor

```
void cond_wait(mon_t *m) {
  assert(m->tid==thread_id());
 int old_count = m->count;
 m\rightarrow count = 0; m\rightarrow tid = 0;
 de_schedule(&m->cond);
 bool next_to_enter;
 \do {
                                         void cond_notify(mon_t *m) {
   atomic {
                                           // wake up other threads
      next_to_enter = m->tid==0;
                                           m->cond = 1;
      if (next_to_enter) {
        m->tid = thread_id();
        m->count = old_count;
   if (!next_to_enter) de_schedule(&m->tid);
 } while (!next_to_enter);
```

A Note on Notify

With signal-and-continue semantics, two notify functions exist:



notifyAll: wakes up all threads waiting on a condition variable



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With signal-and-continue semantics, two notify functions exist:

- notify: wakes up exactly one thread waiting on condition variable
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an implementation often becomes easier if notify means notify some

→ programmer should assume that thread is not the only one woken up

Atomic Executions, Locks and Monitors

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• a notified thread is likely to block immediately on &m->tid

- Notified threads compete for the monitor with other threads
- if OS implements FIFO order: notified threads will run after threads that tried to enter since wait was called
- giving priority to waiting threads requires better interface to OS

Implementing PopRight with Monitors



We use the monitor $q \rightarrow m$ and the condition variable $q \rightarrow c$. PopRight:

```
double-ended queue: removal
 int PopRight(DQueue* q, int val) {
   QNode* oldRightNode;
   monitor_enter(q->m); // wait to enter the critical section
L: QNode* rightSentinel = q->right;
   oldRightNode = rightSentinel->left;
   if (oldRightNode==leftSentinel) { cond_wait(q->c); goto L; }
   QNode* newRightNode = oldRightNode->left;
   newRightNode->right = rightSentinel;
   rightSentingel->left = newRightNode;
   monitor_leave(q->m); // signal that we're done
  int val = oldRightNode->val;
   free(oldRightNode);
   return val;
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```

- if the queue is empty, wait on $q \rightarrow c$
- use a loop, in case the thread is woken up spuriously

Atomic Executions, Locks and Monitors Locked Atomic Executions

Monitor versus Semaphores



protect each queue with a mutex



Monitor versus Semaphores



A monitor can be implemented using semaphores:

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Atomic Executions, Locks and Monitors

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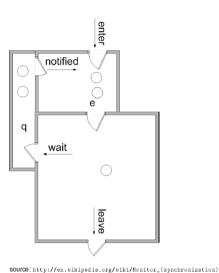
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 - wake up thread and have it check the predicate itself
- create condition variable for each set of threads with the same p
 - notify variable if the predicate may have changed
- or, simpler: notify all threads each time any predicate changes



Monitors with a Single Condition Variable



Monitors with a single condition variable are built into Java and C#:



```
class C {
  public synchronized void f() {
    // body of f
is equivalent to
class C {
  public void f() {
   monitor_enter();
    // body of f
```

with Object containing:

monitor_leave()

private int mon_var; private int mon_count; private int cond var;

protected void monitor_enter(); protected void monitor_leave();

Deadlocks with Monitors



Definition (Deadlock)

A deadlock is a situation in which two processes are waiting for the respective other to finish, and thus neither ever does.

(The definition generalizes to a set of actions with a cyclic dependency.)



Deadlocks with Monitors



Definition (Deadlock)

A deadlock is a situation in which two processes are waiting for the respective other to finish, and thus neither ever does.

(The definition generalizes to a set of actions with a cyclic dependency.)

Consider this Java class:

Sequence leading to a deadlock:

```
class Foo {
  public Foo other = null;
 public synchronized void bar() {
    ... if (*) other.bar(); ...
and two instances:
 Foo(a) = \underline{new} Foo();
  Foo b = \text{new Foo}();
  a.other = b; b.other = a;
  // in parallel:
  a.bar() || b.bar();
```

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Deadlocks with Monitors



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- class Foo { public Foo other = null; public synchronized void bar() { • a.bar() acquires the monitor of a ... if (*) other.bar(); ...
- threads A and B execute a.bar()
 - and b.bar()

and two instances:

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Sequence leading to a deadlock:

- threads A and B execute a.bar() and b.bar()
- b.bar() acquires the monitor of b
- A happens to execute other.bar()
- A blocks on the monitor of b
- B happens to execute other.bar()

Atomic Executions, Locks and Monitors

// in parallel: a.bar() || b.bar();

Foo a = new Foo():

Foo b = new Foo();

a.other = b; b.other = a;

Treatment of Deadlocks



- mutual exclusion: processes require exclusive access
- wait for: a process holds resources while waiting for more
- no preemption: resources cannot be taken away form processes
- orcular wait: waiting processes form a cycle

The occurrence of deadlocks can be:

Treatment of Deadlocks



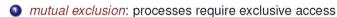
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Deadlocks occur if the following four conditions hold [1]:



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- → *prevention* is the only safe approach on standard operating systems
- can be achieve using lock-free algorithms
- but what about algorithms that require locking?

Deadlock Prevention through Partial Order



Observation: A cycle cannot occur if locks can be partially ordered.

Definition (lock sets)

Let L denote the set of locks. We call $\lambda(p) \subset L$ the lock set at p, that is, the set of locks that may be in the "acquired" state at program point p.

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We require the transitive closure σ^+ of a relation σ :

Definition (transitive closure)

Let $\sigma \subseteq X \times X$ be a relation. Its transitive closure is $\sigma^+ = \bigcup_{i \in \mathbb{N}} \sigma^i$ where

$$\sigma^{0} = \sigma$$

$$\sigma^{i+1} = \sigma^{i} \cup \{\langle x_{1}, x_{3} \rangle \mid \exists x_{2} \in X . \langle x_{1}, x_{2} \rangle \in \sigma^{i} \land \langle x_{2}, x_{3} \rangle \in \sigma^{i} \}$$

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